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February 14, 2019 This guide describes the
clocking resources available in all Spartan-6
devices, including the DCMs and PLLs.

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Xilinx Spartan-6 FPGA SP605 Evaluation Kit

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offers all the basic components for developing broadcast, wireless communications, automotive, and other cost- and power-sensitive applications that require transceiver capabilities in one package. The SP605 evaluation kit provides integration of hardware, software, intellectual property (IP), and pre-verified reference designs so development can begin right out of the box.

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Automation ...

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